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	What is Claimed:
1	1. A circuit for applying a transfer function to an input signal comprising:
2	an input line for receiving the input signal;
3	a plurality of operators for generating piecewise-linear segments of the
4	transfer function; and
5	a window detector for determining a value of the input signal and selecting one
6	of the operators based on the value of the input signal;
7	wherein the selected one of the operators applies a correction value to correct
8	the value of the input signal.
1.	2. The circuit of claim 1 wherein the selected operator generates the
2	piecewise-linear segment free of a table for defining the piecewise-linear segments of the
3	transfer function.
1	3. The circuit of claim 1 wherein each of the operators generates a

- 3. The circuit of claim 1 wherein each of the operators generates a different one of the piecewise-linear segments of the transfer function.
- 4. The circuit of claim 3 wherein each of the operators simultaneously generates a respective correction value responsive to the value of the input signal; and
  - the circuit further including a multiplexer for selecting one of the respective correction values to correct the value of the input signal.
- 5. The circuit of claim 4 wherein the window detector includes a plurality of digital comparators and an encoder for selecting the one respective correction value to correct the value of the input signal.
- 6. The circuit of claim 1 wherein the selected operator includes a multiplier for multiplying the value of the input signal with a value of a slope of the piecewise-linear segment generated by the selected operator.

	1	7. The circuit of claim 1 wherein the selected operator includes a
	2	subtractor, a multiplier and an adder;
	3	the subtractor subtracting a lower value of the piecewise-linear segment,
	4	generated by the selected operator, from the value of the input signal to provide an offset
	5	value;
<u></u>	6	the multiplier multiplying the offset value with a value of a slope of the
psk psk	7	piecewise-linear segment to provide a product; and
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The Road Touch of the Road of	8	the adder adding the product and a low output value of the piecewise-linear
	9	segment to provide the correction value.
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FA FA	1	8. The circuit of claim 1 wherein the input signal is a video signal and the
	2	transfer function is an inverse gamma transfer function.
		and an area of the second seco
Topic Topic Living Living Living money of H committee of the committee of	1	9. A gamma correction circuit for applying an inverse gamma transfer
Hart.	2	function to an input video signal, the circuit comprising:
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	3	an input line for receiving the input video signal;
		and implies the for receiving the input video signal,
	4	a plurality of operators for generating piecewise-linear segments of the inverse
	5	gamma transfer function; and
		S and the same same same same same same same sam
	6	a window detector for determining a value of the input video signal and
	7	selecting one of the operators based on the value of the input video signal;
		on the operators based on the value of the input video signal;
	8	Wherein the selected are of the appropriate and its annual in the appropriate and appropriate and its annual in the appropriate and its an
	9	wherein the selected one of the operators applies a correction value to correct the value of the input video signal.
	Ĩ.	and varied of the input video signal.
	1	10. The circuit of claim 9 wherein the selected operator generator the
	2	and the selection operator generates the
	3	piecewise-linear segment free of a table for defining the piecewise-linear segments of the inverse gamma transfer function.
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segment to provide the correction value.

1	11. The circuit of claim 9 wherein each of the operators generates a
2	respectively different one of the piecewise-linear segments of the inverse gamma transfer
3	function.
1	12. The circuit of claim 11 wherein each of the operators simultaneously
2	generates a respective correction value responsive to the value of the input signal; and
3	the circuit further including a multiplexer for selecting one of the respective
4	correction values to correct the value of the input video signal.
1	13. The circuit of claim 12 wherein the window detector includes a
2	plurality of digital comparators and an encoder for selecting the one respective correction
3	value to correct the value of the input video signal.
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1	14. The circuit of claim 9 wherein the operator includes a multiplier for
2 · · · 3	multiplying the value of the input video signal with a value of a slope of the piecewise-linear
3	segment generated by the selected operator.
1 -	15. The circuit of claim 9 wherein the operator includes a subtractor of
2	15. The circuit of claim 9 wherein the operator includes a subtractor, a multiplier and an adder;
-	months and an actor,
3	the subtractor subtracting a lower value of the piecewise-linear segment,
4	generated by the selected operator, from the value of the input video signal to provide an
5	offset value;
6	the multiplier multiplying the offset value with a value of a slope of the
7	piecewise-linear segment to provide a product; and

the adder adding the product and a low output value of the piecewise-linear